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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of

Inventor: Tetsuya IKEDA et al. Allowed: 09/08/04  
Art Unit: 2188  
Appln. No.: 09/857,022 Examiner: M. Namazi  
Filed: May 31, 2001  
For: INTERLEAVE ADDRESS GENERATION APPARATUS AND  
INTERLEAVE ADDRESS GENERATION METHOD

SUBMISSION

Honorable Commissioner of  
Patents and Trademarks  
Washington, DC 20231

Sir:

The applicants hereby submit copies of a Supplementary European Search Report dated February 24, 2005 and cited references. It is respectfully noted that the Applicants consider these documents as not material to the patentability of any claim of this application, but are submitting these documents merely to place them in the file, as they were cited in a counterpart foreign application.

Entry of these document into the file of the above-captioned patent is respectfully requested.

Respectfully submitted,

James E. Ledbetter  
Registration No. 28,732

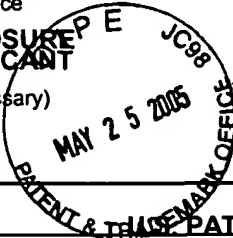
Date: May 25, 2005

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FORM PTO-1449 U.S. Department of Commerce  
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# INFORMATION DISCLOSURE STATEMENT BY APPLICANT

(Use several sheets if necessary)



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09/857,022

APPLICANT

Tetsuya IKEDA, et al.

FILING DATE

May 31, 2001

GROUP

2188

## U.S. PATENT DOCUMENTS

EXAMINER INITIAL	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
	5 5 0 2 6 9 6	03/1996	Yoon et al.			

## FOREIGN PATENT DOCUMENTS

DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION
					YES NO
0 0 3 5 1 0 1	06/2000	WO			
0 6 6 0 5 5 8	06/1995	EP			

## OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

	Supplementary European Search Report dated February 24, 2005.
	"A New Low-Complexity Turbo Code Interleaver Employing Linear Congruential Sequences," TR45.5.3.1/98.12.08.08, XP002237239 pages 1-14, Dec. 8, 1998.
	Nortel Networks, NTTDoCoMo, "Prime Interleaver Complexity Analysis," TDOC R1-99-0513 of 3GPP TSG RAN WG 1, Meeting 4, XP002315002, pages 1-10, Apr. 18, 1999.
	Motorola, "A Proposal for Turbo Code Interleaving," TDOC R1-99-0239 of 3GPP TSG RAN WG 1, Meeting 3, XP002315003, pages 1-8, Mar. 22, 1999.
	Eroz, M. et al., "On the Design of Prunable Interleavers for Turbo Codes," Proc. of Vehicular Technology Conference, Houston, TX, vol. 2, XP010342111, ISBN 0-7803-5565-2, pages 1669-1673, May 16, 1999.

EXAMINER

DATE CONSIDERED

EXAMINER: Initial if citation is considered, draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.